

Fig.1 View of PAC160C.

DESCRIPTION

PAC160C is galvanic separated, 6-bit analog to digital converter, OC type. Built-in signal level detection circuit for input voltage allows force logic states, omitting transient states. **Input signal settling time** is an important parameter. It is set typically to 20ms and can be modified on request of the customer. This time should be equal or greater than output signal settling time of the controller. The hysteresis circuit prevents contacts from "flickering" in switching points. LED diodes indicate output states.

TECHNICAL DATA

Power supply	24 V AC/DC \pm 10%
Max. current consumption	76mA for 24V AC, 35mA for 24V DC
Input resistance	100k Ω
Operating input voltage	0 – 10V
Sensitivity	10mV
Resolution	150mV
Input signal settling time	20ms
Hysteresis width	60mV
Output signal	unpotential contact - OC type
Max. output current	50mA
Max. Collector-Emitter voltage	30V
Max. output power dissipation	150mW
Protection class of the case / terminals	IP-40 / IP-20
Ambient temperature range	-10...+55° C
Diameter of terminals	2,5 mm ²
Protections	against reverse polarization
Mounting	DIN-35 or DIN-32 rail
Dimensions (L x W x H)	96mm x 70,5mm x 42mm
Weight	135 g

PAC160C

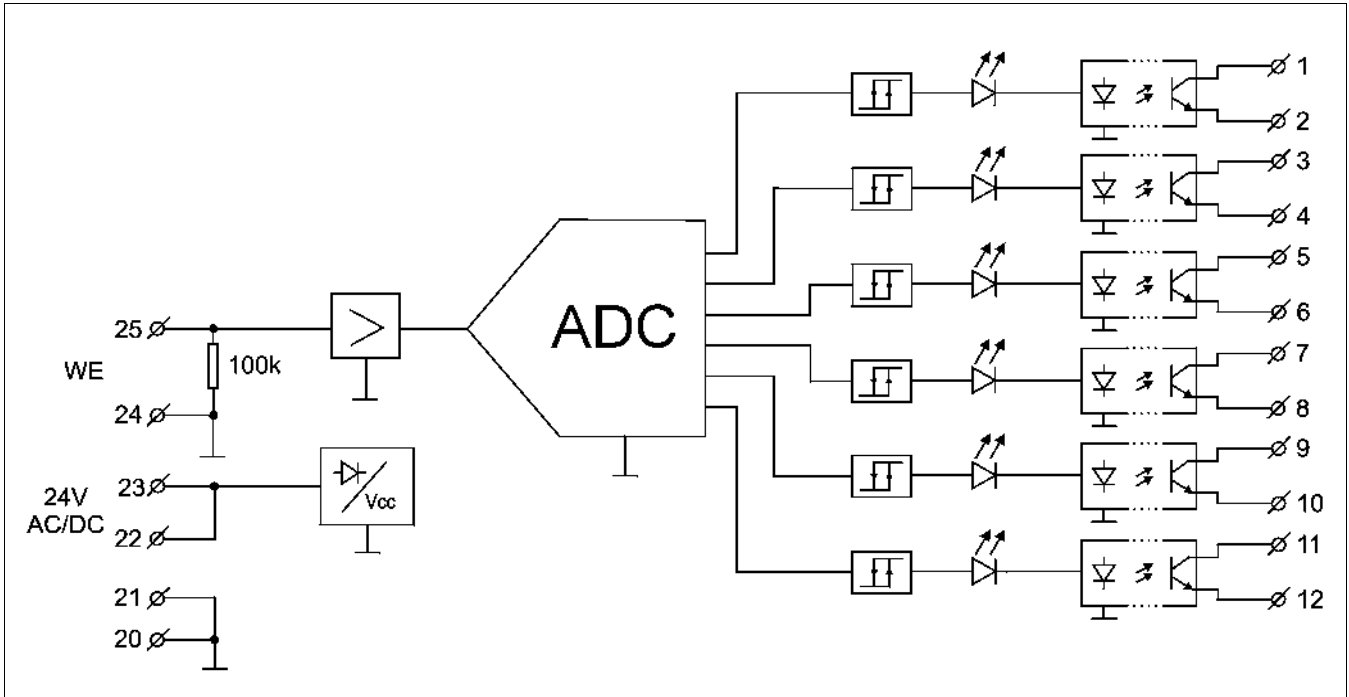


Fig. 2 Connections of PAC160C.

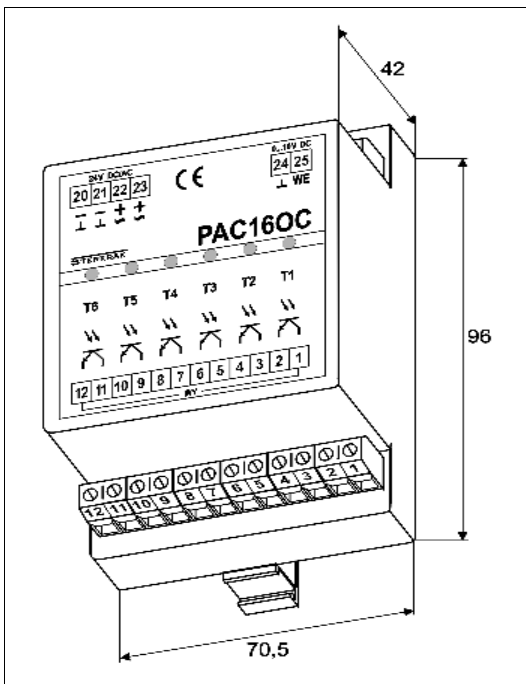


Fig. 3 Dimensions of PAC160C.

U_{WE} [V] rising	U_{WE} [V] falling	T ₆	T ₅	T ₄	T ₃	T ₂	T ₁
0	0	○	○	○	○	○	○
0,18	0,12	○	○	○	○	○	●
0,33	0,27	○	○	○	○	●	○
...
...
9,18	9,12	●	●	●	●	○	●
9,33	9,27	●	●	●	●	●	○
9,48	9,42	●	●	●	●	●	●

adjust accuracy $\pm 0,5\%$

● - transistors conduct, LEDs switched-on

○ - transistors not conduct, LEDs switched-off

ATTENTION:

Hysteresis width, thresholds and input signal settling time can be individually established according to the specification.

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